

## ABSTRACT

A method and associated apparatus is provided for improving the  
5 performance of a high speed memory bus using switches. Bus reflections caused  
by electrical stubs are substantially eliminated by connecting system components in  
a substantially stubless configuration using a segmented bus wherein bus segments  
are connected through switches. The switches disconnect unused bus segments  
during operations so that communicating devices are connected in an substantially  
10 point-to-point communication path.